

Docket: NECN 18.304 (100933-16778)  
Application: Serial No. 09/775,927

**AMENDMENTS TO THE SPECIFICATION**

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*Please amend paragraph 9 of the application-as-filed as follows:*

FIG. 3 shows another quadrature modulator described in Patent Publication JP-A-10-4437, which solves the above problem in the quadrature modulator of FIG. 2. The quadrature modulator of FIG. 3 includes a local oscillator 402 for oscillating at a specified frequency, a first 1/2-frequency-divider 310 for dividing the output frequency of the local oscillator 402 by a factor of two, a second 1/2-frequency-divider 350 cascaded from the first 1/2-frequency-divider 310 for dividing the output frequency thereof by a factor of two, a frequency mixer 320 for frequency conversion using the output frequencies of second 1/2-frequency-divider 350 and the local oscillator 402, a band-pass-filter (BPF) 330 for removing the image signal from the output of the frequency mixer 320, a frequency-multiplier (doubler) 250 for doubling the output of the BPF 330, a third 1/2-frequency-divider 240 for dividing and phase-shifting the output from the BPF 330—frequency multiplier 250 to output a pair of orthogonal carrier waves having a phase difference of 90 degrees therebetween, first and second multipliers 210 and 220 for modulating the carrier waves with a baseband signal generated by a digital signal generator 101, and an adder 230 for adding the outputs of the first and second multipliers 210 and 220 to generate an output digital carrier signal.